



15915 US.PTO
012604

H-1035-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: K. TSUCHIDA
Serial No. 10/086,691
Filed: March 4, 2002

13441 U.S.PTO
10/763187



Group Art: 2812
Examiner: A. Roman
For: MOLD CLEANING SHEET AND MANUFACTURING
METHOD OF A SEMICONDUCTOR DEVICE
USING THE SAME

REQUEST FOR DIVISIONAL APPLICATION
UNDER 37 C.F.R. 1.53(b)

Commissioner for Patents
Mail Stop Patent Application
Alexandria, VA 22313-1450

Sir:

This is a request for filing a divisional application under 37 CFR §1.53(b) of pending prior application Serial No. 10/086,691, filed on March 4, 2002, entitled MOLD CLEANING SHEET AND MANUFACTURING METHOD OF A SEMICONDUCTOR DEVICE USING THE SAME, by all of the inventors named in the prior application.

1. Enclosed is a copy of the prior application, including the Declaration as originally filed.

2. The Filing Fee is calculated below:

CLAIMS AS FILED IN THE PRIOR APPLICATION
LESS ANY CLAIMS CANCELED BY ACCOMPANYING PRELIMINARY AMENDMENT
PLUS ANY CLAIMS ADDED BY ACCOMPANYING PRELIMINARY AMENDMENT

Basic Fee								\$ 770.00	
Total Claims	5	-	20	=	0	x	18	=	0.00
Independent Claims	1	-	3	=	0	x	86	=	0.00
Total Filing Fee								\$ 770.00	

3. A Credit Card Payment Form including the amount of \$770.00 is enclosed to cover the Filing Fee.

4. The Commissioner is hereby authorized to charge any additional fees which may be required, or to credit any overpayment, to Deposit Account No. 50-1417.

5. New drawings are enclosed, eighteen (18) sheets, Figs. 1-23.

6. The power of attorney is set forth in the Declaration in the prior application or an associate power of attorney is hereby granted to:

John R. Mattingly, Registration No. 30,293
Jeffrey M. Ketchum, Registration No. 31,174
Shrinath Malur, Registration No. 34,663
Daniel J. Stanger, Registration No. 32,846
Gene W. Stockman, Registration No. 21,021.

There is no change in the correspondence address.

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Please associate this application with Customer No. 24956.

7. The prior application is assigned to Renesas Technology Corporation and Hitachi Yonezawa Electronics Co., Ltd.

8. Priority of the following Japanese patent application is claimed under 35 U.S.C. § 119:

No. 2001-149827, filed May 18, 2001
No. 2001-213991, filed July 13, 2001.

The certified priority document has been filed in the prior application.

The undersigned hereby declares that no matter contained in the specification, including the claims, and drawings filed in the present divisional application would have been new matter in the prior application Serial No. 10/086,691, as originally filed on March 4, 2002.



Shrinath Malur
Registration No. 34,663
Attorney of Record

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Date: January 26, 2004

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LIST OF INVENTOR'S NAME AND ADDRESS

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